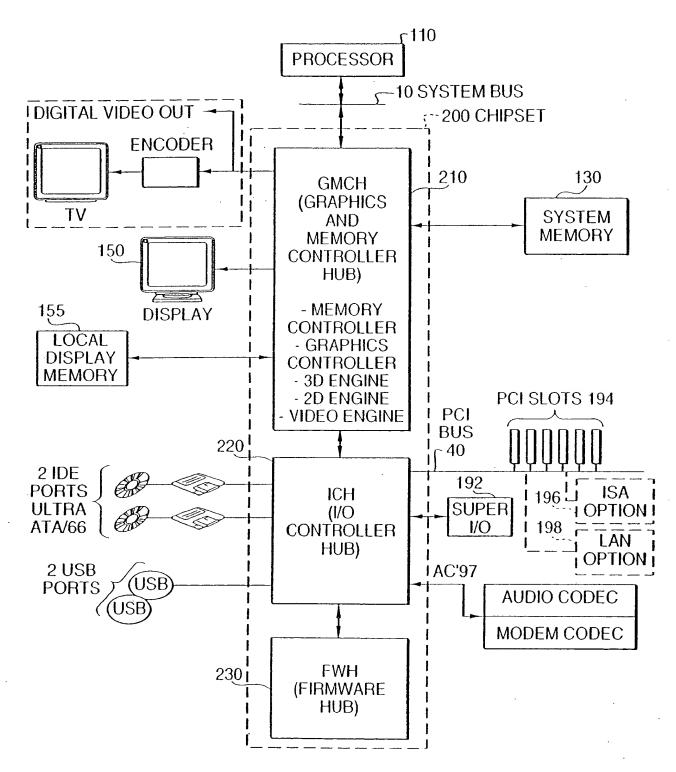
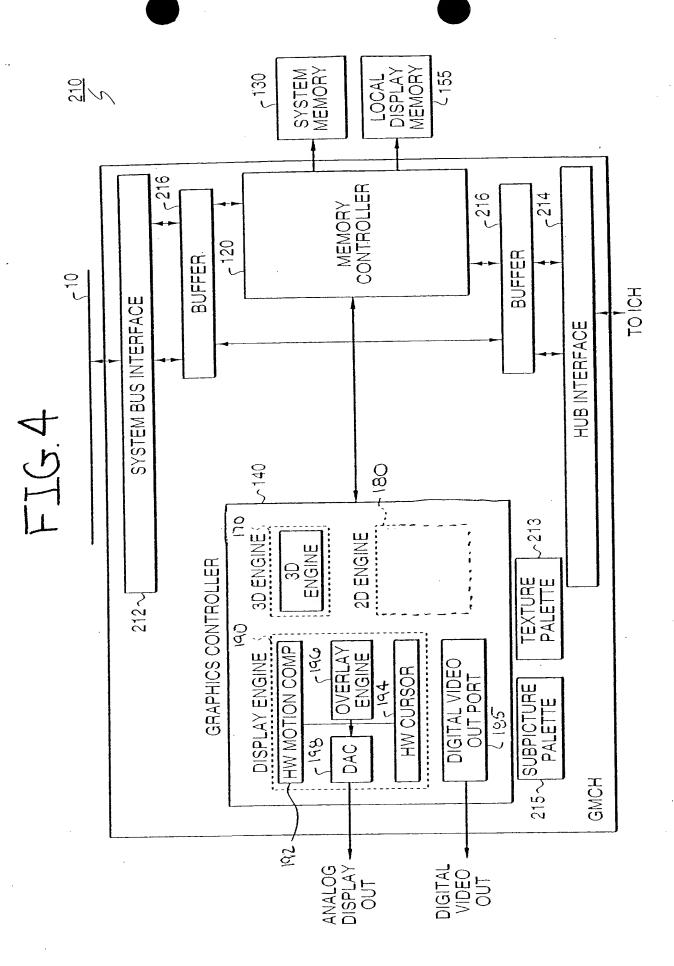
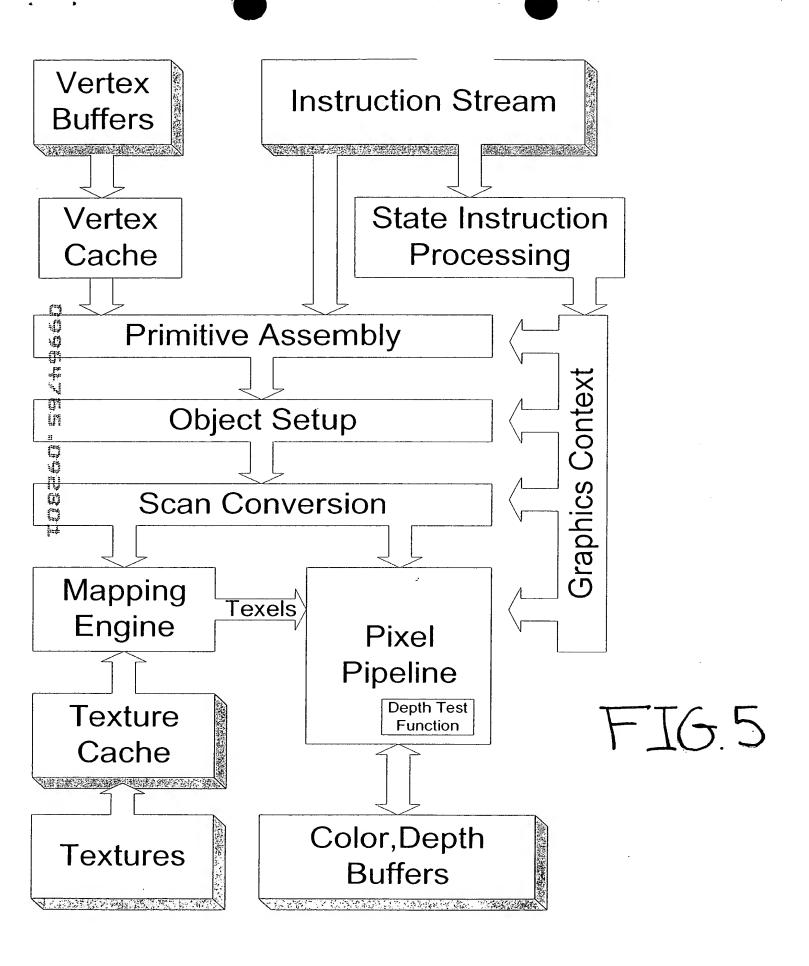


FIG.3







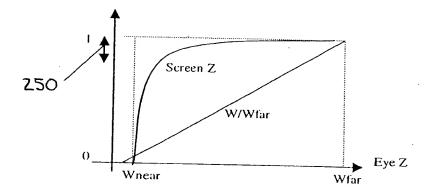


FIG.6

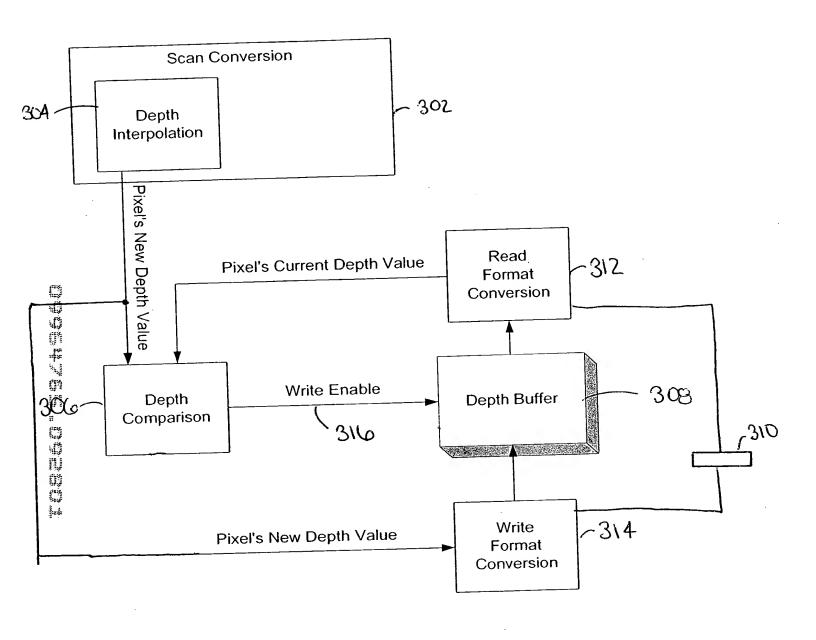


FIG. 7

FIG.8A

15	16-n	15-n 0	
Biased Exponent		Fraction	٦

Bit	Description
15:16-n	Biased Exponent:
L	Format: n-bit unsigned biased exponent, where $n = WExponentSelect$. The exponent is biased by 2^n .
15-n:0	Fraction:
L	Format: (16-n)-bit fractional portion of the floating point significand.

FIG.8B

The first the time the first time the				
			FIG.8B	
D.		. 15		0
			Normalized W	
The state of the s	Bit	·	Description	
	15:0	Normalized W (W/Wfar): Format: U0.16		,
		Range = [0,1)		

FIG.8C

Biased Exponent	Sign	ificand	Represented Value
(n bits)	Integer Fraction		(W/WFar)
$avn = 0.2^{n}-1$	1	Irac	1.frac * 2^(exp-2^n)

FIG.9A

31	24	23	24-n	23-n		0
Stencil		Biased Exponent			Fraction	

Bit	Description
31:24	Stencil:
	Format: U8
	Range = [0,255]
23:24-n	Biased Exponent:
	Format: n-bit unsigned biased exponent, where $n = WExponentSelect$. The exponent is biased by 2^n .
23-n:0	Fraction:
	Format: (16-n)-bit fractional portion of the floating point significand.

FIG.9B

	31		24	23		0
		Stencil			Normalized W	
•					·	

2.2.5		Format: (16-n)-bit tractiona	il portion of the floa	ting point significand.	
terre fine of the state of the					
L.			. ·		
21 g 25 g 22 g 25 g 23 g 25 g			•		
			二十	$C \cap C$	
			I = I	G.9B	
şi				•	
Sec.		31	24 23		0
Ü		Stencil		Normalized W	
N.	<u></u>	T			
The Control of the Line of the Control of the Contr	Bit		I	Description	
fized Stade	31:24	Stencil:			
***		Format: U8 Range = [0,255]			
-	23:0	Normalized W (W/Wfar):			
		Format: U0.24			
_		Range = [0,1)			